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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/625,557	07/24/2003	Shigeo Kigo	P23981	8098
7055 7590 02/22/2007 GREENBLUM & BERNSTEIN, P.L.C. 1950 ROLAND CLARKE PLACE RESTON, VA 20191			EXAMINER BECK, ALEXANDER S	
			ART UNIT	PAPER NUMBER
			2629	
			NOTIFICATION DATE	DELIVERY MODE
			02/22/2007	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

gbpatent@gbpatent.com
pto@gbpatent.com

Advisory Action Before the Filing of an Appeal Brief	Application No. 10/625,557	Applicant(s) KIGO ET AL.	
	Examiner Alexander S. Beck	Art Unit 2629	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 29 January 2007 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
 b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
 (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
 (b) ☐ They raise the issue of new matter (see NOTE below);
 (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 (d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

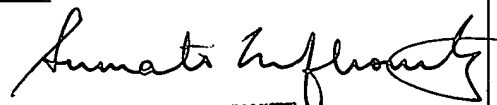
4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
 5. ☐ Applicant's reply has overcome the following rejection(s): _____.
 6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
 7. ☐ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
 The status of the claim(s) is (or will be) as follows:
 Claim(s) allowed: _____.
 Claim(s) objected to: _____.
 Claim(s) rejected: _____.
 Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
 9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
 10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
 12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____.
 13. ☐ Other: _____.


 SUMATI INFRAHANT
 SENIOR PATENT EXAMINER

Continuation of 11. does NOT place the application in condition for allowance because:

As to Claims 14-16 and 23-25, Applicants' arguments have been carefully considered but are not found to be persuasive. Applicants contend the claims are not obvious over KANAZAWA et al. because there would have been no motivation at the time of Applicants' invention to replace the diode of KANAZAWA et al. by a capacitor to reduce a resonance frequency of a switching element.

The Examiner rejected Claims 13-16 and 23-25 with KANAZAWA et al. on the grounds that it is well within the knowledge and skill of those of ordinary skill in the art at the time the invention was made that diodes possess such properties as a capacitive element. Moreover, these diodes with capacitive properties, when added to the parasitic capacitances of the switching elements, would reduce the resonance frequency resulting from the parasitic capacitance of the switching element.

As to Claims 17-22, Applicants' arguments have been carefully considered but are not found to be persuasive. Applicants contend that LAI is directed to non-analogous art (e.g., electric propulsion systems) and fails to provide any motivation for one skilled in the art of plasma displays to look at the art of inverters for a solution to reduce a resonance frequency of a switching element of a display panel. The Examiner respectfully disagrees.

A prior art reference is analogous if the reference is in the field of applicant's endeavor or, if not, the reference is reasonably pertinent to the particular problem with which the inventor was concerned. In re Oetiker, 977 F.2d 1443, 1446, 24 USPQ2d 1443, 1445 (Fed. Cir. 1992).

The Examiner rejected Claims 17-22 with KANAZAWA et al. in view of LAI on the grounds that LAI is reasonably pertinent to the particular problem with which the inventor was concerned. Specifically, capacitors Cr are connected to switches in parallel as lossless snubbers in order to allow a zero-voltage turn-off and to slow the voltage rise rate dv/dt . Therefore, it would have been obvious to modify the teachings of KANAZAWA et al. by including capacitors connected to the switches in parallel, as taught/suggested by LAI. The suggestion/motivation for doing so would have been to reduce losses during turn-off and improve the voltage rate by slowing it down. Moreover, as would have been well known to those of ordinary skill in the art, introducing the capacitors into the switching circuit, as taught/suggested by LAI, would reduce the parasitic resonant frequency or the 'ringing' of the switching circuit because the slower voltage rise rate would inevitably lead to reducing or eliminating of such oscillation.

asb
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